

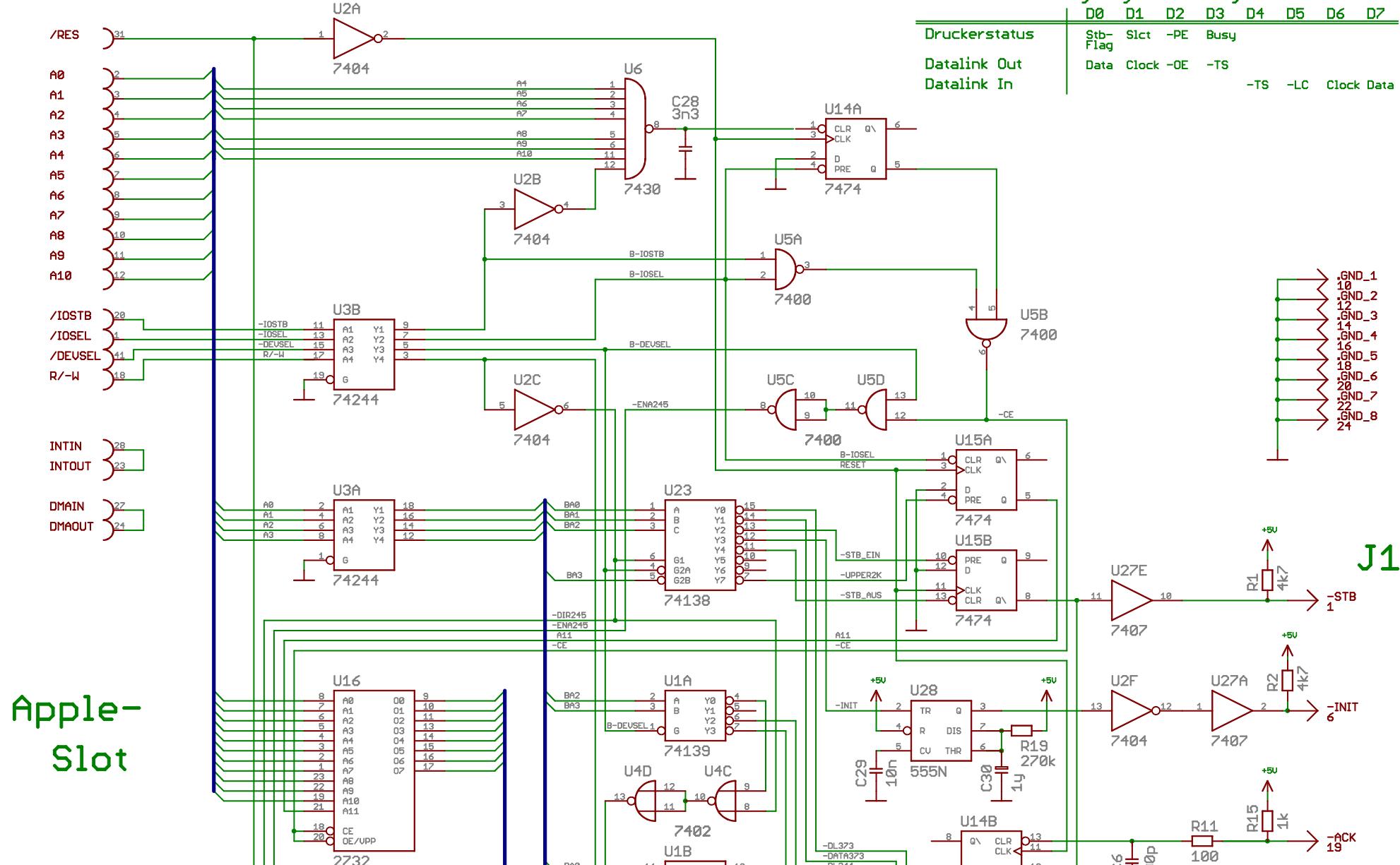
### Belegung der Register

|    |    |    |    |    |    |    |    |
|----|----|----|----|----|----|----|----|
| D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 |
|----|----|----|----|----|----|----|----|

Druckerstatus  
Datalink Out  
Datalink In

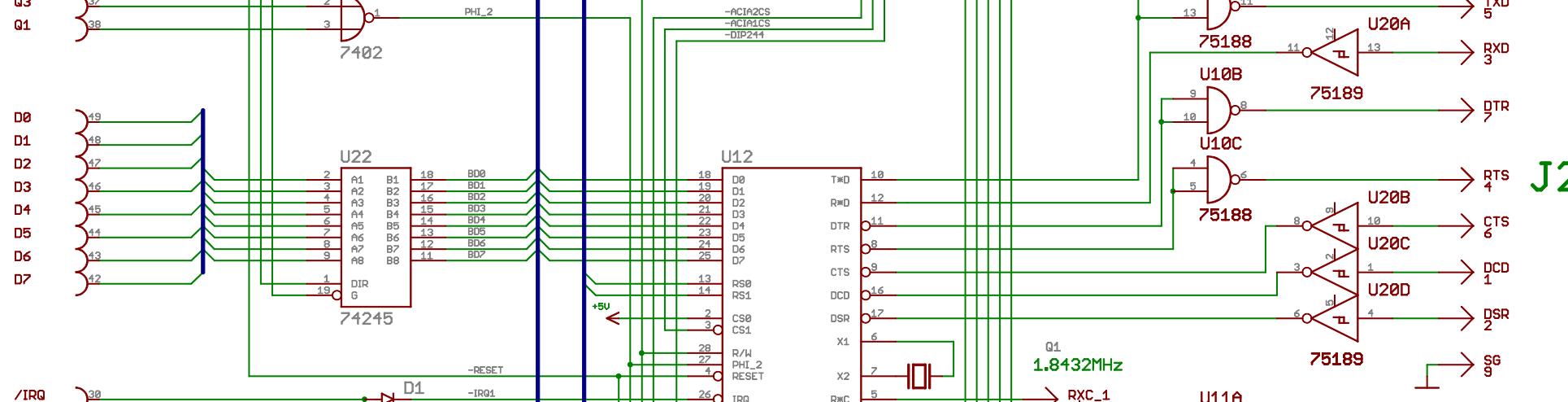
Stb-Flag  
Data Clock -OE -TS

-TS -LC Clock Data

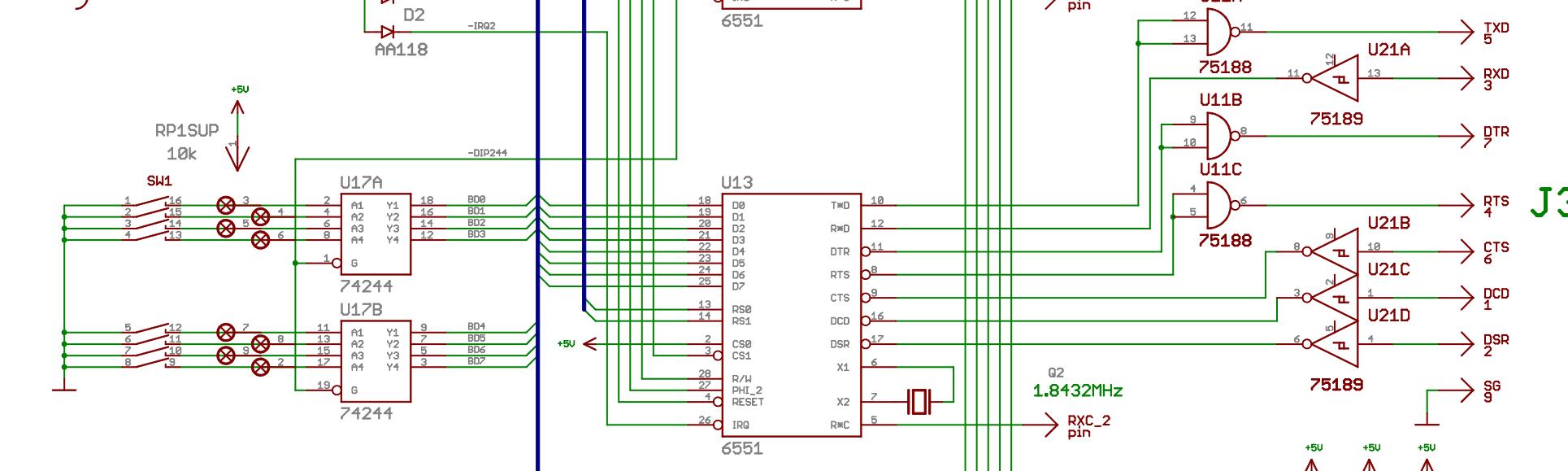


Apple-Slot

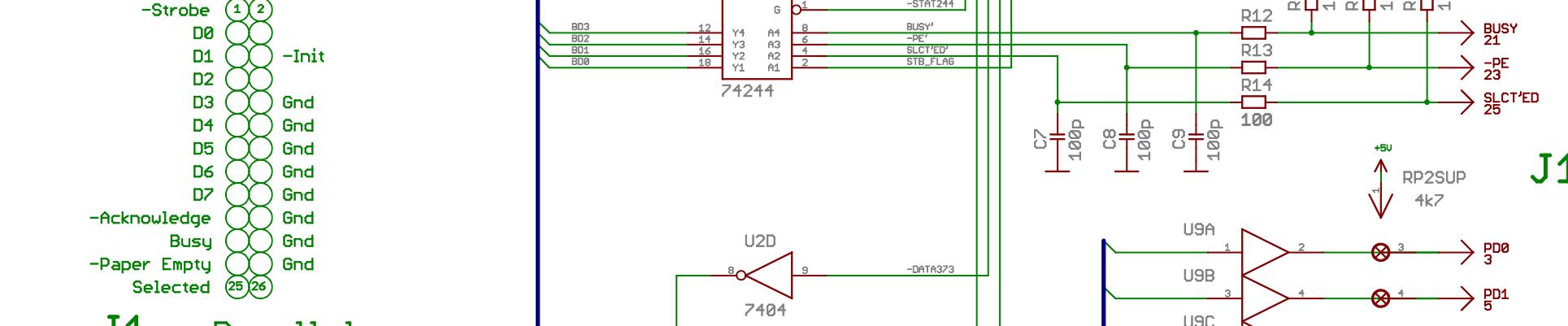
J1



J2



J3



J1

J1 Parallel

DCD  
RxD  
TxD  
DTR  
SG

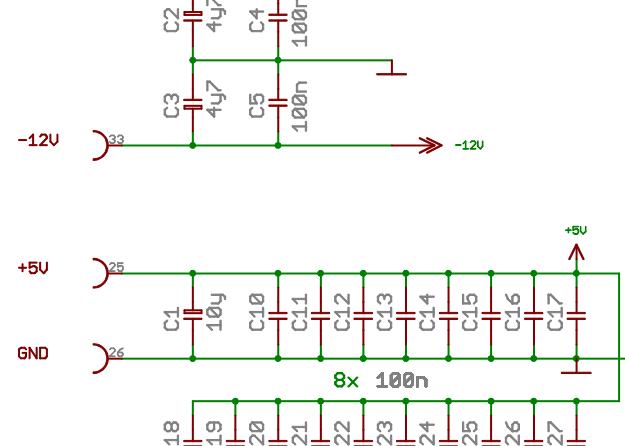
1 2  
3 4  
5 6  
7 8  
9 10  
11 12  
13 14  
15 16  
17 18  
19 20  
21 22  
23 24  
25 26

J2, J3 Serial

-OE  
+5V  
Data out  
Clk out  
-TS out  
-Loop Closed  
+5V  
Data in  
Clk in  
-TS in

1 2 Gnd  
3 4 Gnd  
5 6 Gnd  
7 8 Gnd  
9 10 Gnd  
11 12 Gnd  
13 14 Gnd  
15 16 Gnd  
17 18 Gnd  
19 20 Gnd  
21 22 Gnd  
23 24 Gnd  
25 26 Gnd

J4 Data Link



### Adressbelegung

| Addr.       | Write  | Read   |
|-------------|--|--|
| C0n0        | Datalink Out<br>Druckerdaten<br>-Stb ein (Lo)<br>-Init pulsieren<br>-Stb aus (Hi)<br>auf obere 2k wechseln | Datalink In<br>Druckerstatus<br>DIP-Schalter |
| C0n1        |  |  |
| C0n2        |  |  |
| C0n3        |  |  |
| C0n4        |  |  |
| C0n7        |  |  |
| C0n8 - C0nB | ACIA 1 (J2)  | Datalink In<br>Druckerstatus                 |
| C0nC - C0nF | ACIA 2 (J3)  | DIP-Schalter                                 |

| Addr. | Write  | Read   |
|-------|--|--|
| C0n0  | Datalink Out<br>Druckerdaten<br>-Stb ein (Lo)<br>-Init pulsieren<br>-Stb aus (Hi)<br>auf obere 2k wechseln | Datalink In<br>Druckerstatus<br>DIP-Schalter |
| C0n1  |  |  |